## CARLETON UNIVERSITY

FINAL<br>EXAMINATION

April 2013

Department Name \& Course Number: Electronics ELEC 5705
Course Instructor(s): Prof. John W. M. Rogers
AUTHORIZED MEMORANDA
Calculators, Course Notes
Students MUST count the number of pages in this examination question paper before beginning to write, and report any discrepancy immediately to a proctor. This question paper has 5 pages.

This examination question paper MAY be taken from the examination room.

Information and Instructions:

1. Attempt all questions.
2. Show all analysis.
3. The exam marks total 100.

Potentially Useful Equations:

$$
\begin{aligned}
& v_{D S ~ S a t}=v_{G S}-V_{T}, V=I R, C=\frac{q}{V}, i_{D}=\frac{1}{2}\left(\mu C_{o x}\right)\left(\frac{W}{L}\right)\left(v_{G S}-V_{T}\right)^{2} \\
& v_{D S ~ S a t}=v_{G S}-V_{T}=\sqrt{\frac{2 i_{D}}{\mu C_{o x}}\left(\frac{L}{W}\right)}, E=m c^{2}, c=\frac{1}{\sqrt{\mu \varepsilon}} \\
& r_{D S}=\frac{1}{\lambda I_{D S}}, i_{n d}{ }^{2}=4 k T\left(\frac{2}{3}\right) g_{m}, \mathrm{kT}=4 \cdot 10^{-21} \mathrm{~W} / \mathrm{Hz} \text { at } \mathrm{T}=290 \mathrm{~K}, \mathrm{q}=1.6 \cdot 10^{-19} \mathrm{C} \\
& g_{m}=\sqrt{2 \mu C_{o x}\left(\frac{W}{L}\right) I_{D S}}, \Gamma=\frac{Z_{L}-Z_{o}}{Z_{L}+Z_{o}}, \nabla \times \vec{E}=-\mu \frac{\partial \vec{H}}{\partial t} \\
& a=\frac{d v}{d t}=\frac{d^{2} x}{d t^{2}}, M o n e y=\text { Power }=V I=I^{2} R=\sqrt{E v i l}, R_{o n}=R \cdot \frac{L}{W} \\
& \quad \alpha=\frac{1}{(1+\Delta A) \cos \left(\phi_{e}\right)} \quad \Delta A=\frac{-2\left(Q_{d} Q_{i m}+I_{d} I_{i m}\right)}{I_{d}{ }^{2}+Q_{d}{ }^{2}}, \phi_{e}=2 \tan ^{-1}\left[\frac{Q_{d} I_{i m}-I_{d} Q_{i m}}{Q_{d}{ }^{2}+I_{d}{ }^{2}}\right] \\
& \quad \beta=-\tan \left(\phi_{e}\right) \quad, \quad,
\end{aligned}
$$

## Question 1 (Total 25 Marks)

(a) A nonlinear amplifier with $\mathrm{k}_{1}=2, \mathrm{k}_{2}=0$, and $\mathrm{k}_{3}=0.02$ is driven with: $v_{\text {in }}=x \cdot \cos [2 \pi(10 \mathrm{MHz}) t]$
If the amplifier is matched to $50 \Omega$ at the input and the output at what value of $x$ will the amplifier reach its 1 dB compression point? What will be the value of the output 1 dB compression point in dBm ?
(b) Write an expression for a 1 GHz LO with a 1 V peak amplitude and spurs at 60 dBc at 40 MHz offset.
(c) A signal is modulated with 64QAM and has an RF bandwidth of 1 MHz . What power level can be received with a BER of $10^{-3}$ ?

## Question 2 (Total 25 Marks)

A simple superhertodyne radio is shown below.


You must design the components of the radio to deliver a signal to the base band with a SNR of at least 10 dB . Channels have a bandwidth of 20 MHz and can have power levels from -80 dBm to -30 dBm . Image signals can be as high as -10 dBm . In order to do this fill in the following table at a minimum:

| Component | Parameters |
| :---: | :---: |
| LNA | $\begin{aligned} & \text { Gain: } 15 \mathrm{~dB} \\ & \text { NF: } \quad \mathrm{dB} \\ & \text { IIP3:__dBm } \\ & \hline \end{aligned}$ |
| Image Filter | Insertion Loss: 0 dB <br> Passband: 2.1 GHz <br> Bandwidth: 200MHz <br> Attenuation at 3GHz: |
| RF Mixer | Voltage Gain: 5 dB <br> NF: 10 dB <br> IIP3: <br> : dBm |
| IF Filter | Insertion Loss: 0 dB <br> Passband: 400 MHz <br> Bandwidth: 20 MHz <br> Attenuation: |
| IF Amplifier | Voltage Gain Range: $\qquad$ dB <br> NF: 15 dB <br> IIP3: -20dBm |
| ADC | Input voltage: 1 Vpp <br> Sampling frequency: 40 MHz <br> Number of Bits: 10 <br> Clock timing jitter: |

## Question 3 (Total 25 Marks)

A transmitter is designed to put out a power of 33 dBm . At the same time a receiver is expected to receive a signal at -70 dBm at a frequency 100 MHz away. The phase noise of the transmit LO is $-160 \mathrm{dBc} / \mathrm{Hz}$ at 100 MHz offset. 16QAM OFDM modulation with 64 subcarriers is used with a bandwidth of 10 MHz .
(a) Specify the performance level of the duplexor for this radio.
(b) What is the required linearity of the transmitter to have an EVM of 25 dB ?
(c) What is the required linearity of the transmitter to provide an ACPR of -40 dBc ?
(d) What is the in band phase noise requirement of the LO?

## Question 4 (Total 25 Marks)

Consider an all-digital phase locked loop like the one shown below.

(a) What phase noise would be required for the radio in question \#2?
(b) If a 1 MHz reference is used what is the required TDC resolution?
(c) What resolution is required for the DCO ?
(d) If the natural frequency of the loop is set to 200 kHz , and Kphase $=10^{-3} / 2 \pi$ and $\mathrm{K}_{\mathrm{DCO}}=200 \mathrm{MHz} /$ unit design this loop.

